

AMENDMENTS TO THE SPECIFICATION:

Please amend the Abstract of the Disclosure as follows:

A method for streamlining error correction code computation while reading or programming a NAND flash memory. At least some of the illustrative embodiments are methods comprising transferring a data block between a flash memory and a memory controller, and computing an ECC for said data block while transferring the data block.
A method and apparatus for transferring a data block between a flash memory and a memory controller and computing an error correction code while transferring said data block.